

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): SATO et al

Serial No.: Rule 1.53(b) continuation of U.S. Patent Application Serial
No. 09/709,403 filed November 13, 2000

Filed: Herewith

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group of Parent: 2813

Examiner of Parent: W. Vesperman

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 and § 1.98

Mail Stop DD
Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

November 26, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List of documents cited in prior application Serial No. 09/709,403, filed November 13, 2000.

Since application Serial No. 09/709,403 is being cited under 35 USC §120 in the above-identified continuation application, copies of the listed documents are not enclosed. See 37 CFR §1.98(d).

To the extent that the above-listed documents are not English, the requirements of 37 CFR §1.98(a)(3) are satisfied at least by the English translations enclosed with the documents as submitted in prior application Serial No. 09/709,403.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified Continuation application.

In view of all of the foregoing, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Kindly charge any additional fees due, or credit overpayment of fees, to Deposit Account No. 01-2135 (501.39288CX1).

Respectfully submitted,

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RTW/RTW

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U.S. Department of Commerce

Atty. Docket No.
501.39288CX1

Serial No.
Not yet assigned

INFORMATION DISCLOSURE CITATION
BY APPLICANT
(Use several sheets if necessary)

Applicant
SATO, et al

Filing Date
Herewith

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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	6 2 5 8 6 4 9	07/10/01	Nakumaura et al.			
	6, 2 5 5 1 5 1	06/03/01	Fukuda et al.			
	6 2 6 1 8 8 3	07/17/01	Koubuchi, et al.			
	6 0 2 7 9 8 3	02/22/00	Hashimoto, et al.			
	5 5 3 9 2 4 0	07/23/96	Cronin, et al.			
	6 1 4 3 6 2 6	011/2000	Yabu et al.			
	6 3 3 7 2 8 2	01/2002	Kim et al.			
	5 8 2 7 7 8 2	10/1998	Shih			
	5 0 9 8 8 5 6	03/1992	Beyer et al.			
	6 1 3 3 1 0 5	10/2000	Chen et al.			
	6 0 6 0 3 5 2	05/2000	Sekiguchi et al.			

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	1 4 5 2 8 6	05/99	Japan			Abstract	
	0 0 0 4 7 7 6	03/16/94	Korea				
	5 2 3 5 1 5 7	09/10/93	Japan			X	
	8 5 1 4 6 3 A1	07/01/98	European Patent Office				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1.	

EXAMINER

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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Subclass	Filing Date if Appropriate
	6	4	8	3	1	3	6	11/2002	Yoshida et al.			
	6	1	2	1	0	8	6	09/2000	Kuroda et al.			
	2002	0	1	8	2	7	98	12/2002	Saito et al.			
	6	4	8	3	1	3	6	11/2002	Yoshida et al.			
	6	1	2	1	0	8	6	09/2000	Kuroda et al.			

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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